

Remarks

Applicants respectfully request that the amendments herein made be entered into the application before further examination of the application.

The specification is amended to correct typographical errors.

Claim 10 is amended to replace "a field programmable gate array (FPGA)" with --the logic circuit-- to properly recite the structure of the invention.

Conclusion

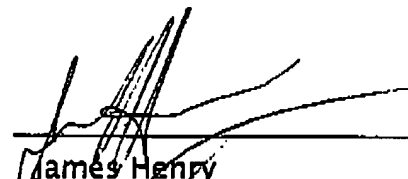
In view of the amendments and remarks made above, it is respectfully submitted that the pending claims are in condition for allowance, and such action is respectfully solicited.

Respectfully submitted,

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